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EMC DESIGN GUIDE FOR ST MICROCONTROLLERS 2.1.2 Latch-Up (LU) 2.1.2.1 Static Latch-Up (LU) test: The Latch-up is a phenomenon which is defined by a high current consumption resulting from an overstress that triggers a parasitic thyristor structure and need a disconnection of the power supply to recover the initial state.

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Study Guide; Not-So-Good EMC Design Guidelines. These design guidelines are either too vague, do not apply often enough to be useful, or are just wrong. Circuit Board Layout A ground plane should completely fill the area under every component without voids, except where necessary for via holes.

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